

L&L-I0232



41

2826

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

By: Manfred Beck

Date: July 7, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Wolfgang Rösner et al.
Applic. No. : 10/047,013 ✓
Filed : January 16, 2002
Title : Method for Producing a Vertical Semiconductor Transistor Component and Vertical Semiconductor Transistor Component
Examiner : Fazli Erdem
Group Art Unit : 2826

R E S P O N S E

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RECEIVED
JUL 17 2003
TECHNOLOGY CENTER 2800

S i r :

Responsive to the Office action dated April 14, 2003 the following remarks are made: